Buffers: How we fell in love with them, and why we need a divorce



Hot Interconnects, Stanford 2004

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Which would you choose?

DSL Router 1



\$50 4 x 10/100 Ethernet 1.5Mb/s DSL connection

1Mbit of packet buffer

DSL Router 2



\$55 4 x 10/100 Ethernet 1.5Mb/s DSL connection

4Mbit of packet buffer

Network religion

Bigger buffers are better

Outline

- How we fell in love with buffers
- Why bigger is not better
 - Network users don't like buffers
 - Network operators don't like buffers
 - > Router architects don't like buffers
 - We don't <u>need</u> big buffers
 - > We'd often be better off with smaller buffers
- Some examples
- How small could we make the buffers?

- Packet switching is good
 - Long haul links are expensive
 - Statistical multiplexing allows efficient sharing of long haul links
- Packet switching requires buffers
- Packet loss is bad
- Use big buffers
- Luckily, big buffers are cheap

Statistical Multiplexing

WARNING!!!

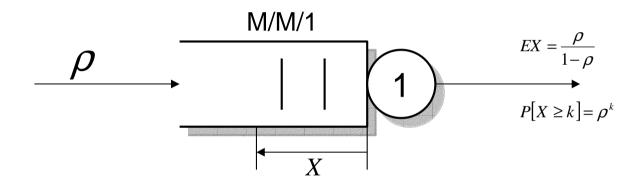
If you see this text, it means you need to have the Shockwave Flash files in the same directory as the powerpoint file. The Flash files are available, with this talk, at:

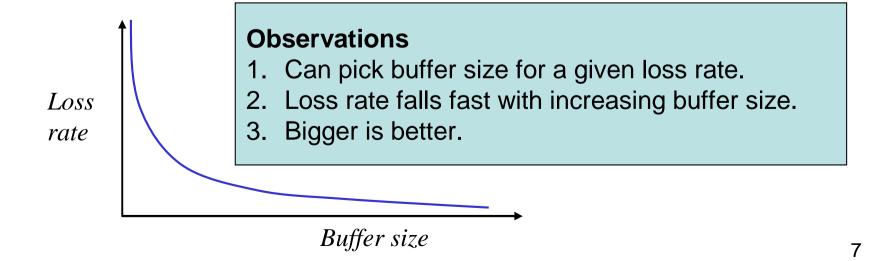
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Observations

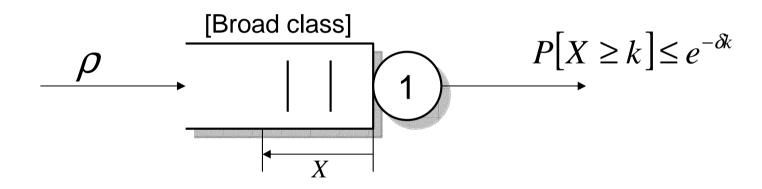
- 1. The bigger the buffer, the lower the packet loss.
- 2. If the buffer never goes empty, the outgoing line is busy 100% of the time.

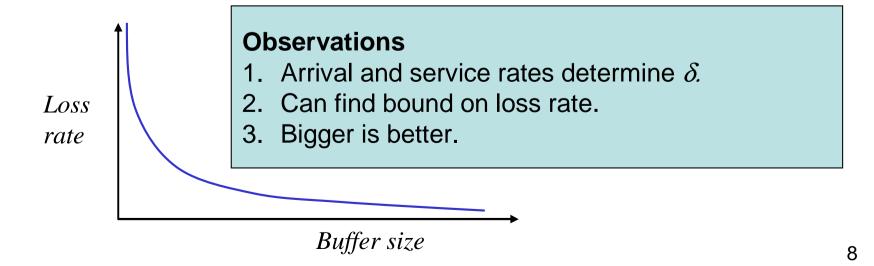
1. Queueing Theory

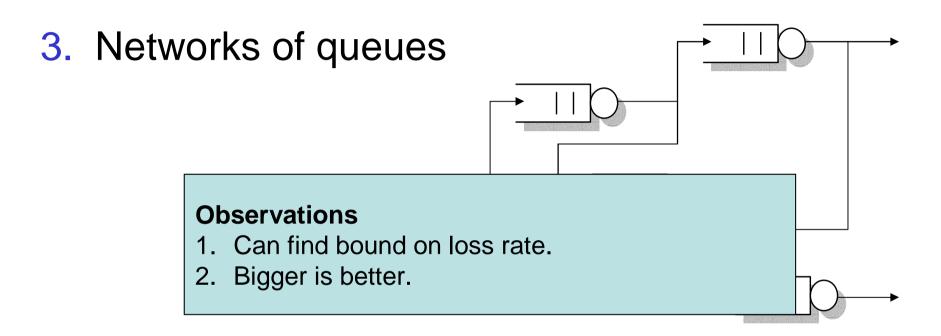




2. Large Deviations







Queueing Theory: Jackson networks, BCMP networks, ...

Large Deviations: Additivity of effective bandwidths, decoupling bandwidth, ...

- Moore's Law: Memory is plentiful and halves in price every 18 months.
 - > 1Gbit memory holds 500k packets and costs \$25.

> Conclusion:

- Make buffers big.
- Choose the \$55 DSL router.

Why bigger isn't better

- Network users don't like buffers
- Network operators don't like buffers
- Router architects don't like buffers
- > We don't need big buffers
- We'd often be better off with smaller ones

Example

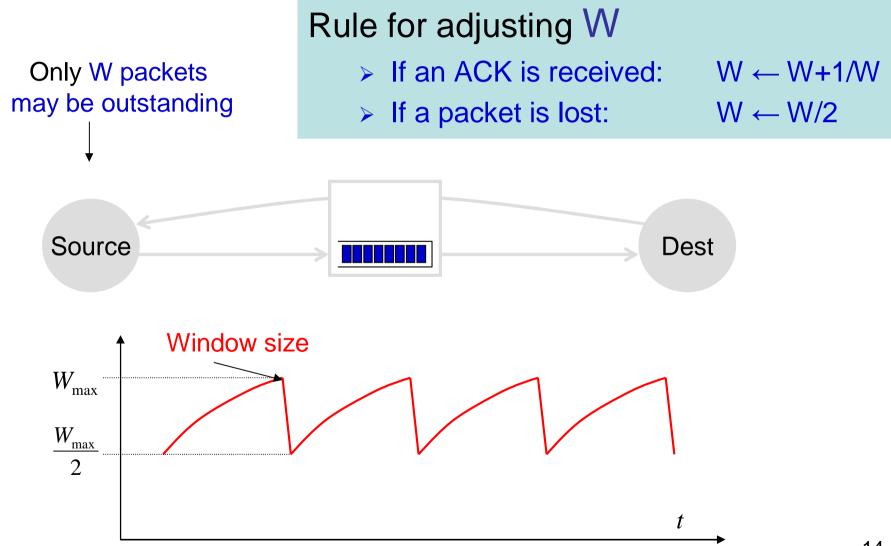
- > 10Gb/s linecard
 - > Rule-of-thumb: 250ms of buffering
 - > Requires 300Mbytes of buffering.
 - Read and write 40 byte packet every 32ns.
- Memory technologies
 - > SRAM: require 80 devices, 1kW, \$2000.
 - > DRAM: require 4 devices, but too slow.
- Problem gets harder at 40Gb/s

Sizing buffers

Packets are generated by a *closed-loop* feedback system

- 95% of traffic is TCP: End-to-end window-based flow control
- Queues with closed-loop source behave very differently
- > TCP requires packet loss. Loss is not bad.
- > Throughput is a better metric.

Review: TCP Congestion Control



Review: TCP Congestion Control

Only W packets may be outstanding

Rule for adjusting W

- ▶ If an ACK is received: W ← W+1/W
- ▶ If a packet is lost:
 W ← W/2

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Some Examples



Example 1:

Make backbone router buffers 99% smaller!

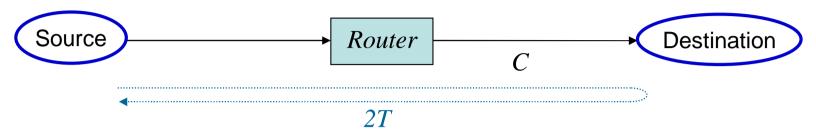
Example 2:

Make access router buffers much smaller too!

Example 3:

Heck, things aren't so bad with no buffers at all.

Backbone router buffers



- Universally applied rule-of-thumb:
 - \triangleright A router needs a buffer size: $B = 2T \times C$
 - 2T is the two-way propagation delay
 - *C* is capacity of bottleneck link

Context

- Mandated in backbone and edge routers.
- > Appears in RFPs and IETF architectural guidelines..
- ➤ Usually referenced to Villamizar and Song: "High Performance TCP in ANSNET", CCR, 1994.
- Already known by inventors of TCP [Van Jacobson, 1988]
- > Has major consequences for router design

Backbone router buffers

- > It turns out that
 - > The rule of thumb is wrong for a core routers today
 - > Required buffer is $\frac{2T \times C}{\sqrt{n}}$ instead of $2T \times C$
- Where does the rule of thumb come from? (Answer: TCP)

Single TCP Flow

Router with large enough buffers for full link utilization

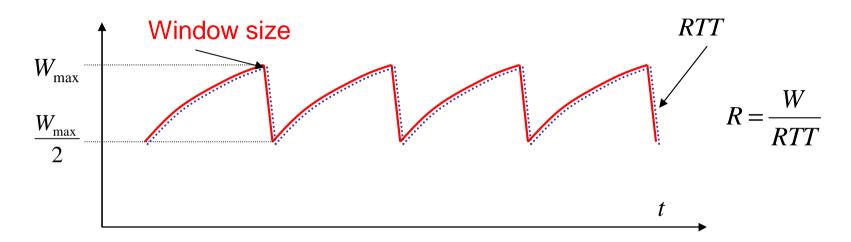
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Single TCP Flow

Router with large enough buffers for full link utilization



Observations

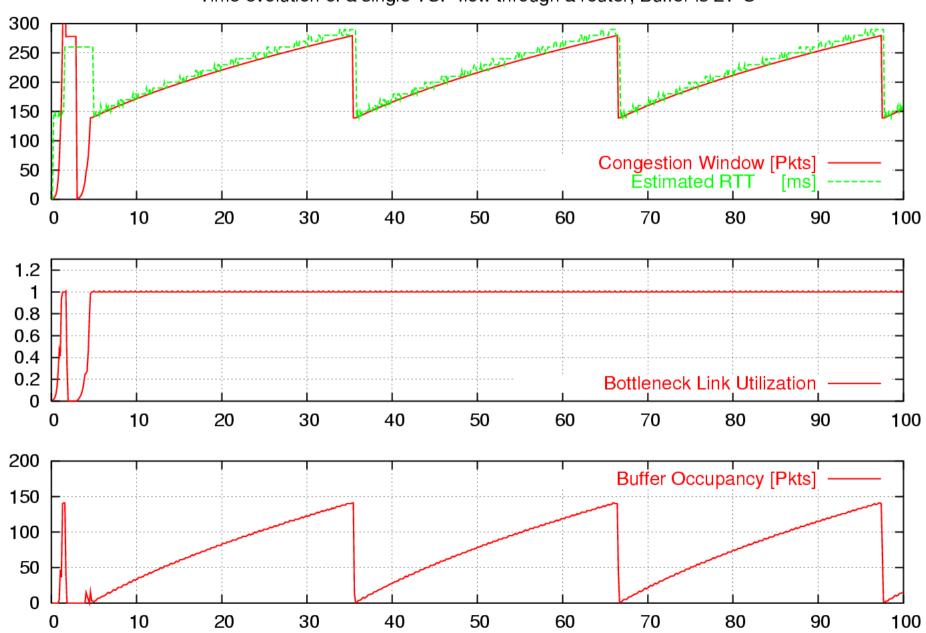
- Sending rate is constant
- If buffer doesn't go empty when window size halves, then we have 100% throughput.

It follows that

$$B = 2T \times C$$

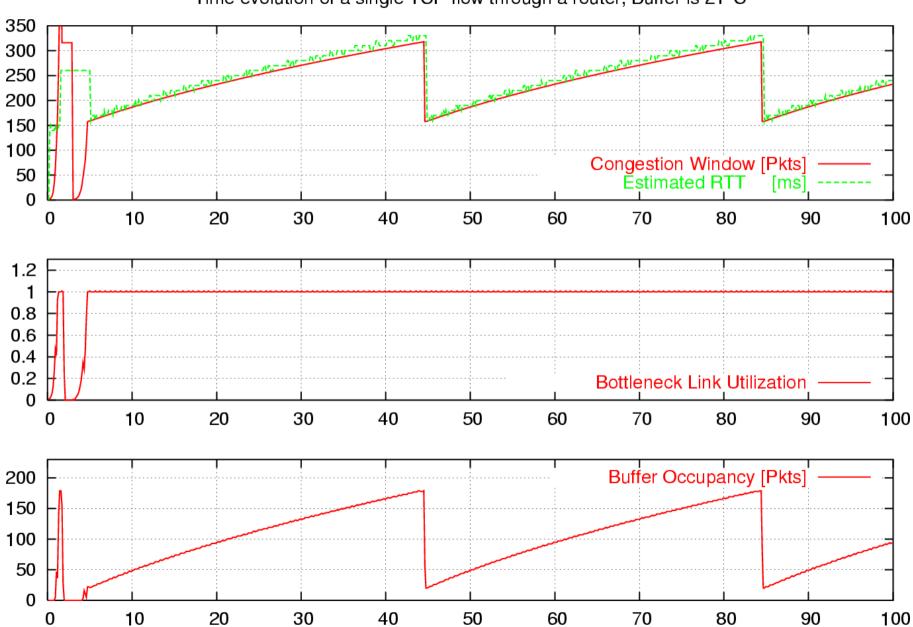
Buffer = rule of thumb

Time evolution of a single TCP flow through a router, Buffer is 2T*C



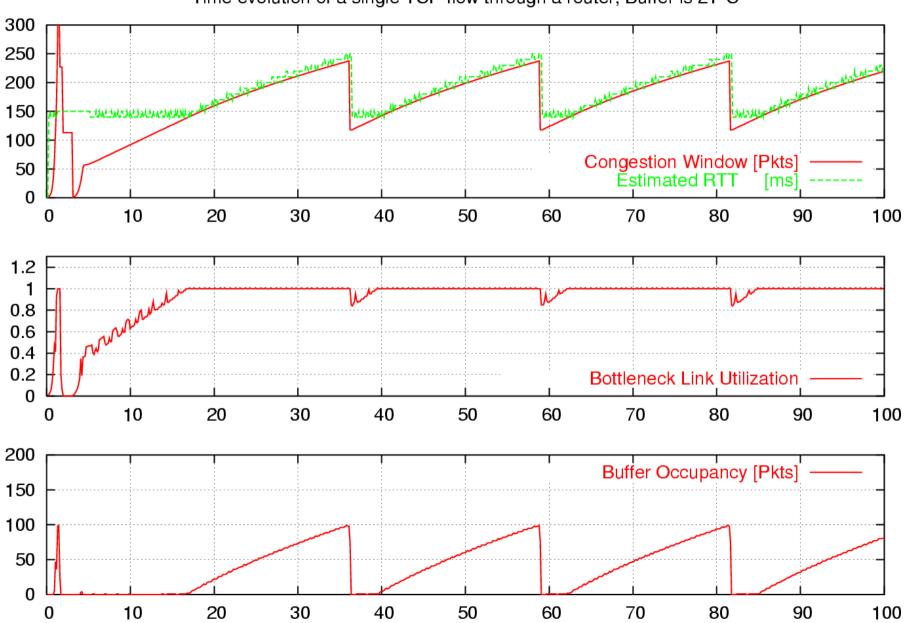
Over-buffered Link

Time evolution of a single TCP flow through a router, Buffer is 2T*C



Under-buffered Link

Time evolution of a single TCP flow through a router, Buffer is 2T*C

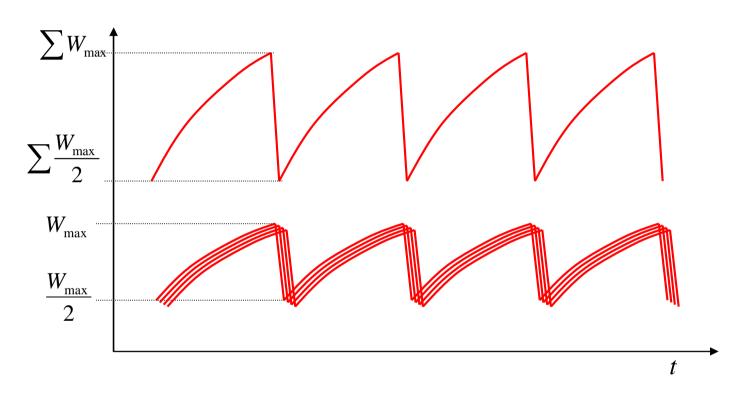


Rule-of-thumb

- Rule-of-thumb makes sense for one flow
- Typical backbone link has > 20,000 flows
- Does the rule-of-thumb still hold?

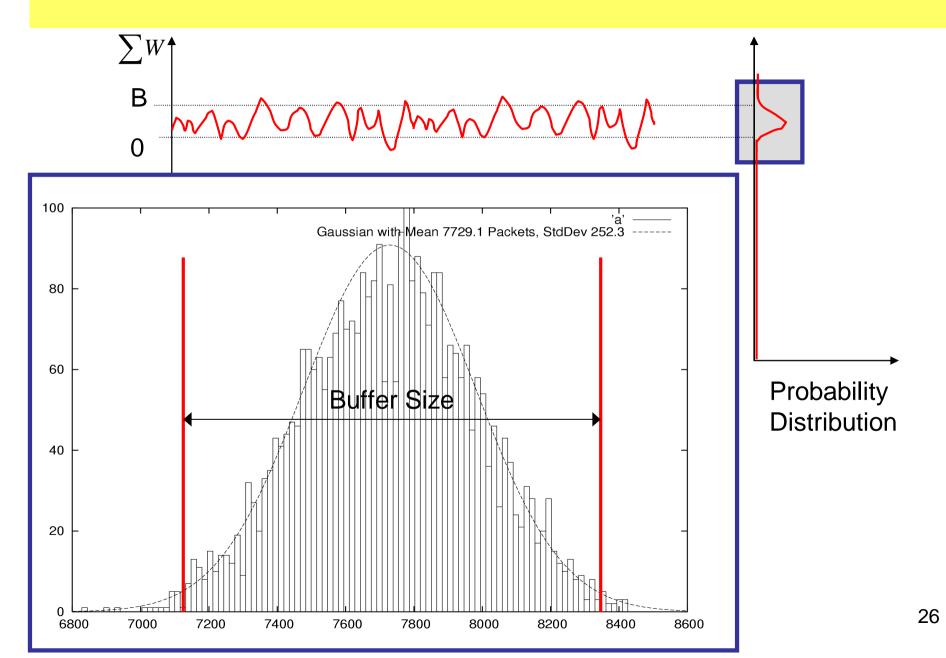
- > Answer:
 - If flows are perfectly synchronized, then Yes.
 - > If flows are desynchronized then No.

If flows are synchronized



- Aggregate window has same dynamics
- Therefore buffer occupancy has same dynamics
- > Rule-of-thumb still holds.

If flows are not synchronized



Quantitative Model

Model congestion window as random variable

$$W_i(t) E[W_i] = \mu_W var[W_i] = \sigma_W^2$$

If congestion windows are independent, central limit theorem tells us

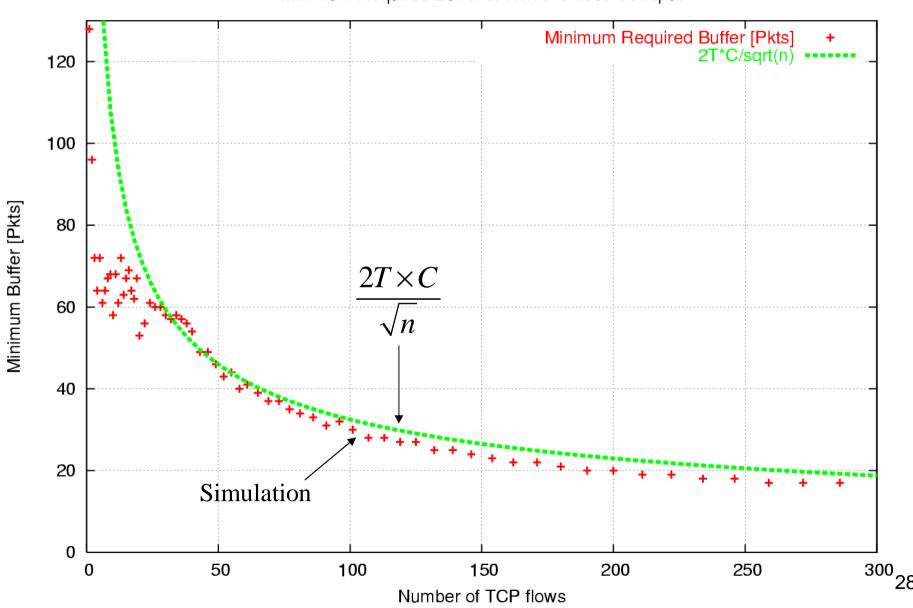
$$\sum_{n} W_{i}(t) \rightarrow \mu_{n=1} + \frac{1}{\sqrt{n}} \sigma_{n=1} N(0,1)$$

> Thus as *n* increases, buffer size should decrease

$$B \rightarrow \frac{B_{n=1}}{\sqrt{n}}$$

Required buffer size

Minimum Required Buffer to Achieve 95% Goodput

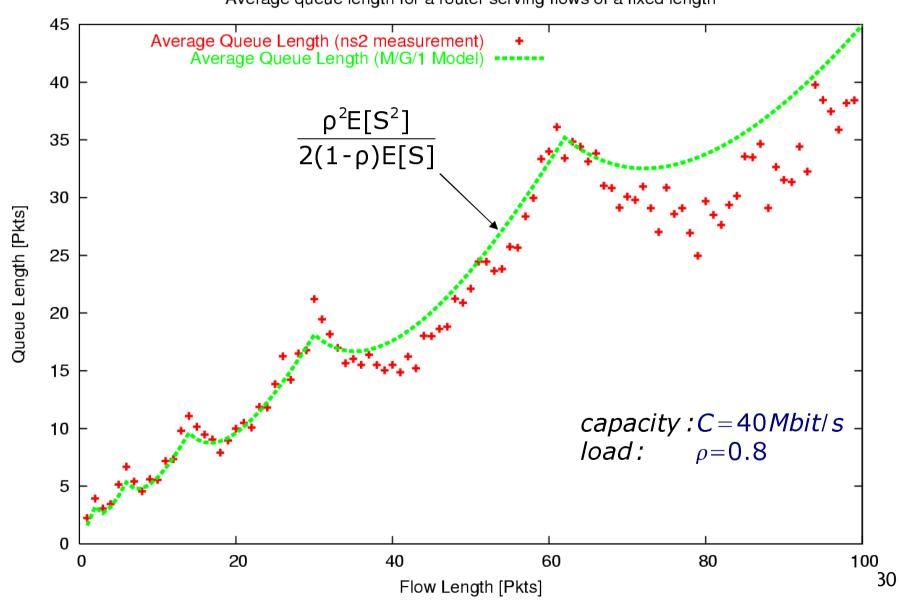


Short Flows

- So far we were assuming a congested router with long flows in congestion avoidance mode.
 - What about flows in slow start?
 - Do buffer requirements differ?
- Answer: Yes, however:
 - Required buffer in such cases is independent of line speed and RTT (same for 1Mbit/s or 40 Gbit/s)
 - > In mixes of flows, long flows drive buffer requirements
 - Short flow result relevant for uncongested routers

Average Queue length

Average queue length for a router serving flows of a fixed length



Queue Distribution

Large-deviation estimate of queue distribution

$$P(Q > b) = e^{-b\kappa} \qquad \kappa = \frac{2(1-\rho)}{\rho} \frac{E[S]}{E[S^2]}$$

$$P(Q > b)$$
Buffer B
Packet
Loss

Short Flow Summary

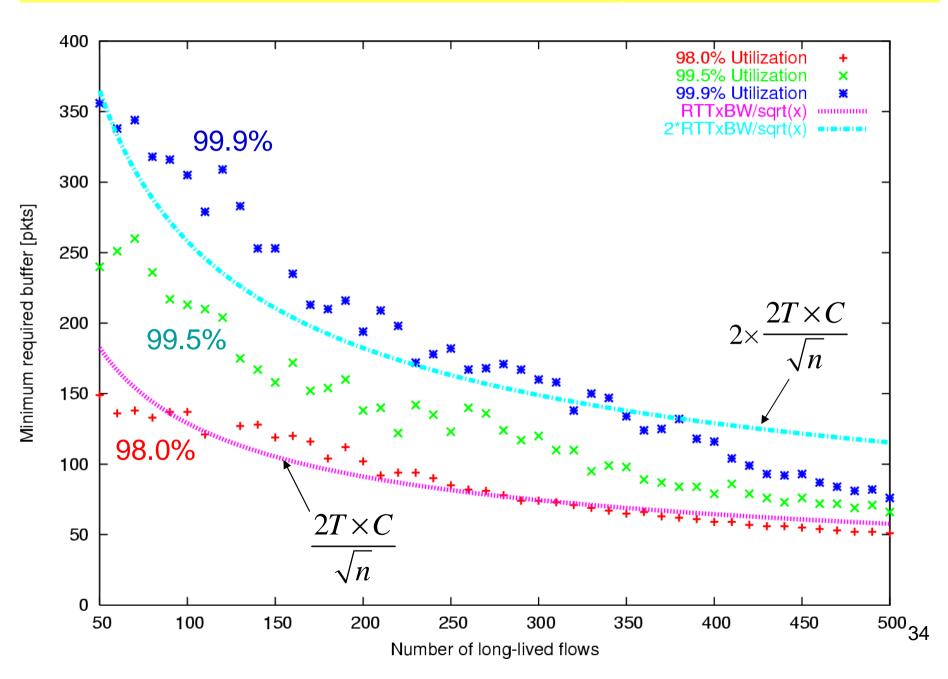
- Buffer requirements for short flows
 - Independent of line speed and RTT
 - Only depends on load and burst size distribution
 - > Example for bursts of up to size 16 at load 0.8
 - For 1% loss probability B = 115 Packets
 - For 0.01% loss probability B = 230 packets etc.
 - Bursts of size 12 is maximum for Windows XP
- In mixes of flows, long flows dominate buffer requirements

Experimental Evaluation

- Simulation with ns2
 - Over 10,000 simulations that cover range of settings
 - Simulation time 30s to 5 minutes
 - Bandwidth 10 Mb/s 1 Gb/s
 - Latency 20ms -250 ms,
- Physical router
 - Cisco GSR with OC3 line card
 - In collaboration with University of Wisconsin
- Operational Networks
 - Stanford University
 - > Internet 2

Long Flows - Utilization

Small Buffers are sufficient - OC3 Line, ~100ms RTT



Long Flows – Utilization Model vs. ns2 vs. Physical Router GSR 12000, OC3 Line Card

| TCP | Router Buffer | | | Link Utilization | | |
|-------|--------------------------------|------|-------|------------------|-------|-------|
| Flows | $\frac{2T \times C}{\sqrt{n}}$ | Pkts | RAM | Model | Sim | Exp |
| 100 | 0.5 x | 64 | 1Mb | 96.9% | 94.7% | 94.9% |
| | 1 x | 129 | 2Mb | 99.9% | 99.3% | 98.1% |
| | 2 x | 258 | 4Mb | 100% | 99.9% | 99.8% |
| | 3 x | 387 | 8Mb | 100% | 99.8% | 99.7% |
| 400 | 0.5 x | 32 | 512kb | 99.7% | 99.2% | 99.5% |
| | 1 x | 64 | 1Mb | 100% | 99.8% | 100% |
| | 2 x | 128 | 2Mb | 100% | 100% | 100% |
| | 3 x | 192 | 4Mb | 100% | 100% | 99.9% |

Operational Networks

1. Stanford University Dorm Traffic 20Mb/s

| ТСР | | uter ıffer | Link Utilization | | |
|-------|--------------------------------|---------------|---------------------|-------|--|
| Flows | $\frac{2T \times C}{\sqrt{n}}$ | Pkts | Model | Exp | |
| 333- | 0.8 x | 46 | 98.0% | 97.4% | |
| 1800 | 1.2 x | 65 | 100% | 97.6% | |
| | 1.5 x | 85 | 100% | 98.5% | |
| | >>2 x | 500 | 100% | 99.9% | |

- 2. Internet2 10Gb/s link, Indianapolis → Kansas City
 - > Cut buffers from 1 second to 5ms (99.5%)
 - ➤ Measured loss: < 10⁻⁷
 - > Even for 6Gb/s transfers from CERN to SLAC.

Impact on Router Design

- > 10Gb/s linecard with 200,000 x 56kb/s flows
 - > Rule-of-thumb: Buffer = 2.5Gbits
 - Requires external, slow DRAM
 - > Becomes: Buffer = 6Mbits
 - Can use on-chip, fast SRAM
 - Completion time halved for short-flows
- > 40Gb/s linecard with 40,000 x 1Mb/s flows
 - Rule-of-thumb: Buffer = 10Gbits
 - Becomes: Buffer = 50Mbits

Some Examples

Example 1:

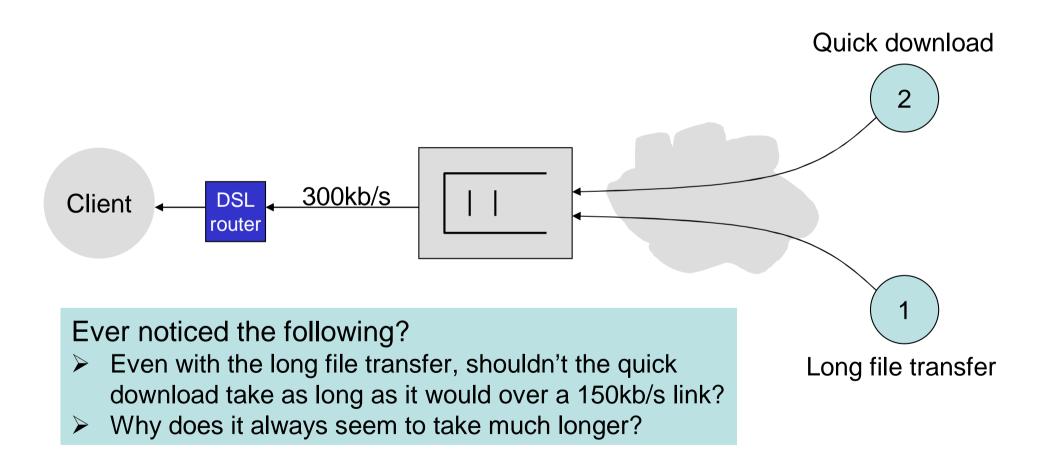
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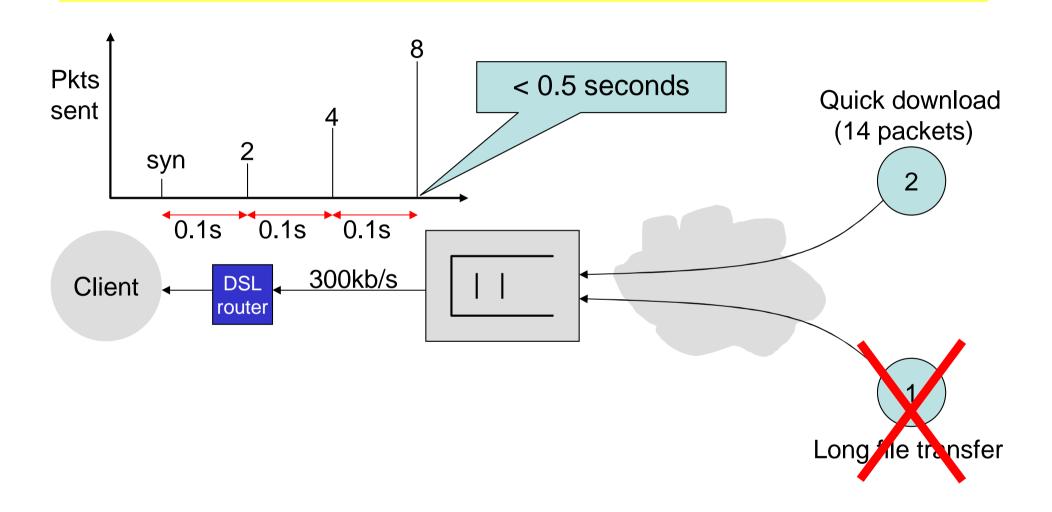
Example 2:

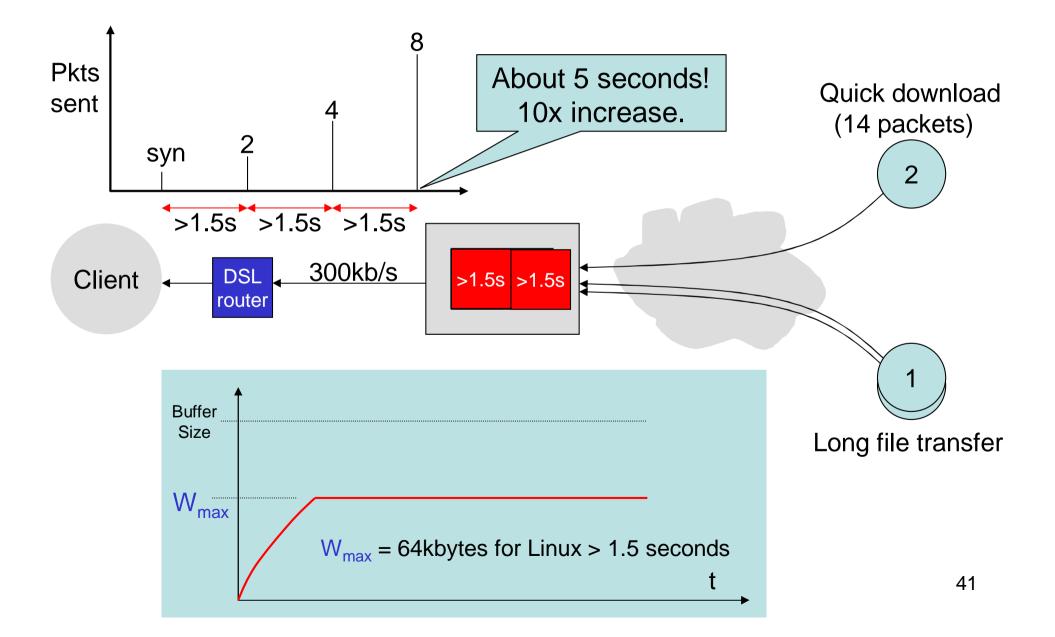
Make access router buffers much smaller too!

Example 3:

Heck, things aren't so bad with no buffers at all.







Observations

- 1. With less buffering:
 - Downloads would complete faster
 - Long transfers would be unaffected
- Because the access router is the bottleneck, packets aren't buffered in the core.
 - We could reduce or remove the core buffers

Some Examples

Example 1:

Make backbone router buffers 99% smaller!

Example 2:

Make access router buffers much smaller too!

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TCP with no buffers

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Summary

- Buffering and queueing delay is everything
- We don't really understand buffer sizing in the Internet...

> ...We need more research.

Many thanks to Guido Appenzeller at Stanford. New Flash expert. For more details, see our Sigcomm 2004 paper available at:

http://www.stanford.edu/~nickm/papers

How small can buffers be?

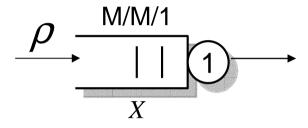
- Imagine you want to build an all-optical router for a backbone network
- It's very hard to build optical buffers...maybe 5-10 packets in delay lines?

LASOR Project

- DARPA funded since 2004
- Partners: UCSB, Cisco, JDSU, Calient, Agility, Stanford
- Program: Building high capacity all-optical router with 40Gb/s interfaces
- > Problem:
 - If you can design the routing mechanism, packet scheduling, and congestion control, then how small can we make the buffers?

What if...

Theory (benign conditions)



$$EX = \frac{\rho}{1 - \rho}$$
 $\rho = 50\%, EX = 1 \text{ packet}$ $\rho = 75\%, EX = 3 \text{ packets}$

$$P[X \ge k] = \rho^k$$
 $\rho = 50\%, P[X > 10] < 10^{-3}$
 $\rho = 75\%, P[X > 10] < 0.06$

Practice



Typical OC192 router linecard buffers over 1,000,000 packets

What if we randomize launch times and paths, so traffic looks Poisson...?